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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	
		Filing Date	
		First Named Inventor	Alter, Martin
		Art Unit	
		Examiner Name	
Sheet	1	of	2
		Attorney Docket Number	M085

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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					T6

Examiner Signature	<i>N. Steinbauer</i>	Date Considered	07/20/04
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Substitute for form 1449B/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	
		Filing Date	
		First Named Inventor	Alter, Martin
		Group Art Unit	
		Examiner Name	
Sheet	2	of	2
		Attorney Docket Number	M085

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials'	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s) publisher, city and/or country where published	T2
M	-	GARROU, Wafer-Level Packaging Has Arrived, Semiconductor International, Oct. 2000, 119 et seq	
D	-	YOUNG, Wafer-level Processing, May 1997, Chip Scale Review May 1997, 28 et seq.	
M	-	NGUYEN et al., Assembly Considerations for SMD Wafer-Level CSPs, Chip Scale Review, May/June 2000, 48 et seq.	
D	-	CHAN et al., Integrated Antennas on Si, ..., IEDM 10-903, 0-7803-7050-3/01/\$10.00 copr. 2001 IEEE, 40.6.1 et seq.	
D	-	PENNWELL CORP., Is THIS the industry's next killer app?, http://sst.pennnet.com/Articles/ , ... 7/14/2003, 7pp.	
M	-	LIHUI et al., High Q Multilayer Spiral Inductor on Silicon Chip for 5-6 GHz, IEEE Electron Device Letters, Vol. 23, No. 8, August 2002, 470 et seq.	

Examiner Signature	<i>Nehi Parell</i>	Date Considered	07/20/04
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